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(71) Applicant (for all designated States except US): **INTEL CORPORATION** [US/US]; 2200 Mission College Boulevard, Santa Clara, CA 95052 (US).

(72) Inventors; and

(75) Inventors/Applicants (for US only): **GUO, Xiaofeng** [CN/CN]; Room 301, Building 13, No.451, Xianxia Road, Shanghai 21000 (CN). **DAI, Jinquan** [CN/CN]; Room 309, Block 2, Ren Le Xin Cun, Songjiang District, Shanghai 210000 (CN). **LI, Long** [CN/CN]; Room 202, Building 34, #123, Fuguan Road, Shanghai 210000 (CN). **LV, Zhiyuan** [CN/CN]; Room 202, Building 8, Yongila St., Luwan District, Shanghai 21000 (CN).

(74) Agent: **CHINA PATENT AGENT (H.K) LTD.**; 22/F, Great Engle Centre, 23 Harbour Road, Wanchai, Hong Kong (CN).

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(54) Title: LATENCY HIDING OF TRACES USING BLOCK COLORING

(57) Abstract: An embodiment of the present invention is a technique to hide latency in program traces. Blocks of instructions between start and end of a critical section are associated with color information. The blocks correspond to a program trace and containing a wait instruction. The wait instruction is sunk down the blocks globally to the end of the critical section using the color information and a dependence constraint on the wait instruction.



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